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PTO/SB/123 (04-05)

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Patent Number

6,946,898

Issue Date

September 20, 2005

Application Number

10/810,039

Filing Date

March 26, 2004

First Named Inventor

Donald A. Kerth

Attorney Docket Number

23119.7

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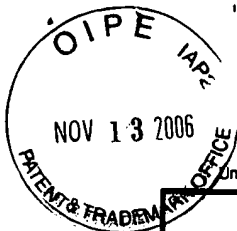
512-464-9295

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below\*.

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PTO/SB/98 (09-04)

Approved for use through 07/31/2008. OMB 0651-0031

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**STATEMENT UNDER 37 CFR 3.73(b)**Applicant/Patent Owner: Silicon Laboratories Inc.Application No./Patent No.: 10/810,039 6,946,898 Filed/Issue Date: March 26, 2004 September 20, 2005Entitled: System and Method For Biasing Electrical CircuitsSilicon Laboratories Inc., a corporation

(Name of Assignee)

(Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest; or
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- A. ☐ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel \_\_\_\_, Frame \_\_\_\_, or for which a copy thereof is attached.

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- B. ☒ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

1. From: Donald A. Kerth, et alTo: Silicon Laboratories Inc.The document was recorded in the United States Patent and Trademark Office at Reel 015159, Frame 0726, or for which a copy thereof is attached.

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The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

Nestor Ho

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Nestor Ho

Printed or Typed Name

General Counsel

Title

11/1/06

Date

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